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<td><strong>Publication date</strong></td>
<td>2018-12-03</td>
</tr>
<tr>
<td><strong>Publication information</strong></td>
<td>IEEE Transactions on Industrial Electronics, 66 (10): 8253-8262</td>
</tr>
<tr>
<td><strong>Publisher</strong></td>
<td>IEEE</td>
</tr>
<tr>
<td><strong>Item record/more information</strong></td>
<td><a href="http://hdl.handle.net/10197/10662">http://hdl.handle.net/10197/10662</a></td>
</tr>
<tr>
<td><strong>Publisher's statement</strong></td>
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<tr>
<td><strong>Publisher's version (DOI)</strong></td>
<td>10.1109/TIE.2018.2883266</td>
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Neutral Current Minimization Control for Solid State Transformers under Unbalanced Loads in Distribution Systems

Junru Chen, Student Member, IEEE, Tao Yang, Cathal O’Loughlin and Terence O’Donnell, Member, IEEE

Abstract—This paper analyses the neutral current reduction performance of a three phase four leg solid state transformer (SST) under different degrees of unbalanced load. Several kinds of control strategies are presented, the neutral current elimination controls which rely on phase shifting, voltage amplitude and phase shifting & voltage amplitude combination control. A neutral current minimization control which ensures the SST output voltages complies with the EN 50160 output voltage unbalance standard is also developed. These control approaches simply build on the balanced voltage control providing voltage references which slightly unbalanced the voltage amplitude and phase angle or both. The effectiveness of the proposed strategies is validated through tests on a downscaled prototype. Simulation results for the neutral current minimization control of the SST applied to a real urban distribution network with distributed loads are presented. The results of this analysis show that overall the neutral current minimization results in an energy saving from both reduced losses in the distribution cables and reduced power consumption in the load.

Index Terms—Neutral Current, Solid State Transformer, Smart Transformer, Unbalanced Loads, Distribution Systems.

I. INTRODUCTION

Improvements in the performance of power electronics devices coupled with the move towards a smarter more controllable grid has motivated interest in the development of the Solid State Transformer (SST) for applications in the distribution system [1][2]. Unlike the conventional line frequency transformer, the SST is an active controllable device which offers the potential for input-output decoupling, active voltage regulation and power flow control. The SST has been proposed as an important element in managing the incorporation of distributed energy resources (DER) [3][4] and storage systems [5][6]. However to date full scale prototype SSTs have only achieved efficiencies in the range of 96.75% [7][8]. Clearly this is less than the typical quoted efficiency for a line frequency distribution transformer (LFT) which is in the range of 98%-99.5%. To compensate the losses, the ancillary services to enhance grid operation which can be provided by the SST, is emphasized in the concept of the smart transformer (ST) [9]. Among the ancillary services investigated in recent literature is the ability to provide on-demand reactive power support for the MV grid [10], power management [11][12] and stability [13] in microgrids [14], maintenance of the stability of the LV grid in the presence of increasing penetration of DER [15][16], online load identification [17] and control of distribution system power generation [18][19] and consumption [20]. Following in this research vein of investigating the system level benefits of the controllable ST, this work investigates the application of an ST to the reduction of neutral currents which arise due to unbalanced loading in the distribution network.

Unbalanced loading is quite common in the modern distribution grid with the growth of dynamically varying domestic loads. Such unbalance may well increase in the future with the introduction of new loads such as electric vehicle charging points [21]. The problems associated with unbalanced loads in the distribution system have been discussed previously in the literature by Jouanne et al. [22]. For example in a 4-wire distribution systems, the unbalance loads can cause excessive current in the neutral wire, thus giving rise to voltage drop along the neutral wire resulting in ground voltage fluctuation for customers [23][24]. Furthermore, excessive neutral current could produce increased losses in the neutral wire. However, it is possible that an SST implementing an appropriate control function, could reduce or eliminate this excessive neutral current by regulating its output voltage. For example, in the context of unbalanced loads in data centers, reference [25] and [26] described a concept and provided three different control methods to eliminate the neutral current by the means of dynamically adjusting the relative amplitudes and the mutual phase differences of the three phase voltages respectively. However, in a distribution system context, the allowable degree of unbalance in the utility supply voltages must adhere...
to standards such as EN 50160 [27], so that a wide variation in relative voltage amplitude and phase between phases is not allowable. The contribution of this work is in the development of a neutral current minimization control strategy for the SST which meets the requirement of voltage supply standards- EN 50160 [27] and is also shown to have the potential for energy saving from the aspect of the whole distribution system. The energy saving possible when the method is applied to an example urban distribution system with a typical unbalanced load profile is investigated.

The paper is structured as follows: Section II briefly reviews the solid state transformer in the distribution system. Section III reviews the neutral current elimination control strategy for the three phase four wire SST under unbalanced loads. Section IV introduces the proposed neutral current minimization control which ensures adherence to the requirements for voltage unbalance in the distribution system. Section V presents hardware validation of the control method on a 220 V 4 kVA downscaled prototype of a 3-phase 4-leg inverter. Finally, section VI provides simulation results for the implementation of the control approach in an actual distribution network with distributed loads and assesses the energy savings possible while Section VII draws the conclusions.

II. SST CONFIGURATION

Similar to the traditional transformer, the SST provides a step-up or step-down voltage function, but with advanced functionality. In this work we use a three phase version of the three stage SST found to be one of the most suitable in terms of input/output decoupling by Falcons et al. [28]. Fig.1 shows the basic configuration of the 3-stage SST consisting of an AC/DC rectifier, Dual Active Bridge (DAB) DC-DC converter with a high frequency transformer and a DC/AC inverter. The rectifier converts the medium voltage grid frequency three-phase AC input voltage into a medium voltage DC. The next step consists of a Dual Active Bridge (DAB) that transforms the medium DC voltage using a high frequency transformer, to a low voltage DC output. Finally, an inverter at the output stage converts the low DC voltage to a power frequency three-phase AC voltage connected to the load.

Fig. 2. Overall three-Phase four-leg SST topology.

III. NEUTRAL CURRENT ELIMINATION CONTROL

In order to illustrate the approach to neutral current elimination or minimization, consider that the distribution system supplied by the SST can be simplified to be represented by three single phase constant impedance loads, Z_a, Z_b and Z_c as shown in Fig. 2.

Assuming that the SST output voltages are given by the balanced three phase supplies:

\[
v_a = V_a \cos(\omega t), \quad v_b = V_b \cos\left(\omega t - \frac{2\pi}{3}\right), \quad v_c = V_c \cos\left(\omega t + \frac{2\pi}{3}\right) \tag{1}
\]

Then the corresponding phase currents will be:

\[
\begin{align*}
    i_a &= I_a \cos(\omega t - \theta_{2a}) \\
    i_b &= I_b \cos\left(\omega t - \frac{2\pi}{3} - \theta_{2b}\right) \\
    i_c &= I_c \cos\left(\omega t + \frac{2\pi}{3} - \theta_{2c}\right)
\end{align*}
\tag{2}
\]

Where \(\theta_{2a}, \theta_{2b}, \theta_{2c}\) are the load angles of the phases, a, b and c and the phase currents are generally unbalanced.

The unbalanced phase currents could be balanced by adjusting the voltage phase angles, amplitudes or both. Consider that we can control the angles and amplitudes of two of the SST output phase voltages relative to the other as follows:

\[
\begin{align*}
    v_a &= V_a \cos(\omega t) \\
    v_b &= \rho_b V_a \cos\left(\omega t - \frac{2\pi}{3} + \gamma_b\right) \\
    v_c &= \rho_c V_a \cos(\omega t + \frac{2\pi}{3} + \gamma_c)
\end{align*}
\tag{3}
\]

Where \(\rho_b, \rho_c\) scale the amplitude of phase b and c respectively relative to phase a, and \(\gamma_b, \gamma_c\) introduce an extra phase shift in phase b and c respectively thus introducing an unbalance in the voltage. Previous works [25][26] have shown that the phase currents can be balanced by the use of the phase shift angles \(\gamma_b, \gamma_c\) alone (with \(\rho_b = \rho_c = 1\), by use of the amplitude scaling factors, \(\rho_b, \rho_c\) alone (with \(\gamma_a = \gamma_b = 0\)) or by a combination of both phase shift and amplitude scaling. For example, in [25] it was shown that using phase shifting alone, if the angles of the phase currents \((\theta_a, \theta_b, \theta_c)\) are set as:

\[
\begin{align*}
    \theta_a &= -\theta_{2a} \\
    \theta_b &= \arccos\left(-\frac{l_a^2 - l_b^2 - l_c^2}{2l_a l_b}\right) - \pi - \theta_{2b} \\
    \theta_c &= \arccos\left(-\frac{l_b^2 - l_c^2 - l_a^2}{2l_b l_c}\right) - \theta_{2c}
\end{align*}
\tag{4}
\]

Then the resulting currents are balanced and the neutral current is eliminated. This implies that the required phase shift for the voltages would be:

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\[ y_{0b} = \theta_b + \theta_{zb} + \frac{2\pi}{3}, \quad y_{0c} = \theta_c + \theta_{zc} - \frac{2\pi}{3} \]  

Note that this is subject to the constraint that
\[ \left| \frac{I^2_a-I^2_b-I^2_c}{2I_A} \right| < 1, \quad \left| \frac{I^2_b-I^2_c-I^2_a}{2I_B} \right| < 1 \]  

Which means \( I_a \leq I_b + I_c, I_b \leq I_a + I_c, I_c \leq I_b + I_a \), i.e. no phase current amplitude should be greater than the sum of the other two.

Alternatively, making use of amplitude scaling alone (i.e. \( y_b = y_c = 0 \)), then the neutral current can be eliminated by using the amplitude scaling factors:
\[ \rho_b = \frac{\sin(\theta_a - \theta_b)}{\sin(\theta_c - \theta_b)} * \frac{|Z_b|}{|Z_a|}, \quad \rho_c = \frac{\sin(\theta_a - \theta_c)}{\sin(\theta_b - \theta_c)} * \frac{|Z_c|}{|Z_a|} \]  

Note that in general the reference phase can be taken as any of the phases and in the case of amplitude scaling it may be more appropriate to choose the reference phase as the phase with the minimum amplitude current. However, for ease of notation here we have assumed that this is phase \( a \).

The neutral current could also be eliminated by combining both phase shifting and amplitude scaling. Using this approach the concept is that voltage amplitude control determines the amplitudes of the resulting currents which meet the necessary constraints for phase shift control in (6). After the amplitude of the reference voltage is selected and consequently the amplitude of the phase current is determined, and then the phase angle can be calculated by phase shift control to eliminate the neutral current. The full details of voltage references calculation for the three control strategies can be found in [25] and [26].

IV. NEUTRAL CURRENT MINIMIZATION CONTROL

The problem with the methods above for application in a distribution system is that standards [27] place limits on the allowable voltage unbalance. For example, the EN 50160 [27] standard which applies to the voltage characteristics in public distribution systems specifies that the unbalanced voltage degree in the distribution system should be below 2% for 95% of the time, with the voltage unbalance defined as the ratio of the negative sequence to the positive sequence component [27]. The estimation of this voltage unbalance degree can be obtained according to (8) and (9) [31].

\[ \text{PVUR} = \frac{V_{\text{max}} - V_{\text{min}}}{V_{\text{avg}}} \times 100\% \]  

\[ \text{UBF} = \frac{V_p}{V_p} \times 100\% \]  

Where, \( V_{\text{max}}/V_{\text{min}}/V_{\text{avg}} \) is the maximum/minimum/average value of the amplitude of output voltage and \( V_p/V_n \) is the negative/positive sequence of output voltage.

The maximum allowable value is 2% for both of these measures of unbalance. This makes it unlikely, especially for larger load unbalances, that the calculations in (8) and (9) give phase angles and amplitudes which are within the allowable range of unbalance. Hence in this case the neutral current cannot be completely eliminated. However, applying a combination of phase shifting and amplitude control on the SST output voltage, which still adheres to these limits still, has the potential to reduce and minimize the neutral current.

A. Neutral Current Minimization with Constraints

The amplitude unbalance degree limitation restricts the voltage amplitude, and phase unbalance degree limitation mainly restricts the phase shifting angle of the voltage. Therefore, the approach to reduce neutral current proceeds in two steps, first the output voltage amplitude references are obtained, and subsequently the phase angle references obtained. In the first step the amplitudes of the voltages are regulated to make the amplitudes of the resulting currents meet the constraints required for phase shift control as outlined in (6) and the amplitudes of the voltages are also regulated for the minimization of neutral current within the amplitude unbalance limitation. As long as the phase currents meet the constraints for phase shift control, the zero neutral current solution could be obtained by the phase shift control as in (5). However, as the reference phase angles obtained from this solution for zero neutral current may violate the phase unbalance constraints, it is necessary to calculate phase shift angles, \( \gamma_b, \gamma_c \) which minimize the neutral current but still satisfy the constraint in (9).

B. Voltage Amplitude Reference Calculation

Making use of voltage scaling factors then the amplitudes of the resulting currents can be written as:

\[ I_a = \frac{V_a}{|Z_a|}; I_{bm} = \rho_b \frac{V_a}{|Z_b|}; I_c = \rho_c \frac{V_a}{|Z_c|} \]  

There are several conditions which the choice of the relative amplitude factors must satisfy. According to (6) in order to ensure that a solution exists for zero neutral current under phase shift control, the relative amplitude correction factors \( \rho_b, \rho_c \) need to be chosen in the following range:

\[ \frac{1}{Z_a} \leq \rho_b + \rho_c \leq \frac{1}{Z_b} + \frac{1}{Z_c}, \quad \frac{\rho_b}{Z_b} \leq \frac{1}{Z_a} + \frac{\rho_c}{Z_c} \leq \frac{1}{Z_a} + \frac{\rho_b}{Z_b} \]  

In addition if \( V_{\text{min}}, V_{\text{max}} \) are the minimum and maximum allowable amplitude values of the output phase voltages for the system then it should be ensured that the voltages do not fall outside this range, i.e. :

\[ \min\{V_{\text{max}}, \rho_bV_{\text{max}}, \rho_cV_{\text{max}}\} > V_{\text{min}} \]  

The voltage amplitude unbalanced constraint of (8), can be rewritten in terms of the factors, \( \rho_b, \rho_c \) as:

\[ \max(1, \rho_b + \rho_c) - \min(1, \rho_b + \rho_c) \leq 2\% \]  

The scaling factors are chosen as the maximum allowable which still satisfy (13). According to [20][32], the load is positively dependent on voltage. Thus, voltage reduction could help to reduce the current or demand. Here we set the scaling factor of the phase with largest load to the minimum value of 0.98, and of the phase with smallest load to the reference phase \( a \). The remaining phase voltage is set to 0.99.

C. Phase Shifting Reference Calculation

Now consider the relationship between the voltage phase
unbalance constraint in (9) which is constrained to be less than 2% and the relative phase shift angles. The voltage phase unbalance constraint, the positive and negative sequence voltage components and the voltage amplitude unbalance constraint can be represented by the following equations:

\[
\begin{align*}
\text{UBF} &= \left| \frac{V_n}{V_p} \right| \times 100\% \leq 2\% \quad (14) \\
V_n &= V_{ma} \angle 0 + V_{mb} \angle (-120 + \gamma_b) \times 1 \leq -120 \\
&+ V_{me} \angle (120 + \gamma_e) \times 1 \leq 120 \\
V_p &= V_{ma} \angle 0 + V_{mb} \angle (-120 + \gamma_b) \times 1 \leq 120 \\
&+ V_{me} \angle (120 + \gamma_e) \times 1 \leq 120 \\
0.98 \times \max(V_{ma}, V_{mb}, V_{me}) &\leq \min(V_{ma}, V_{mb}, V_{me}) \quad (17)
\end{align*}
\]

We want to use the voltage phase unbalance constraint to determine the limitation for phase shifting angle \( \gamma_b \) and \( \gamma_e \). By expanding (15) and (16) and making the assumption that \( \sin(\gamma) = 0 \) for small \( \gamma \), the voltage unbalance in (14) can be written as (18), which is valid for small values of \( \gamma_b \) and \( \gamma_e \):

\[
\frac{|V_n|^2}{|V_p|^2} = \frac{1 + \rho_b^2 + \rho_c^2 - \rho_b \cos(\gamma_b) - \rho_c \cos(\gamma_e) - \rho_b \rho_c \cos(\gamma_b + \gamma_e)}{(1 + \rho_b + \rho_c)^2}
\]

Making use of (18) Fig. 3 plots the UBF for of \( \gamma_b \) and \( \gamma_e \) in the range of +/- 5 degrees and for amplitude scaling factors, \( \rho_b = \rho_c = 1 \). Also plotted is the plane representing the 2% limitation on UBF.

The intersection of the UBF and the 2% limit form an elliptical boundary for \( \gamma_b \) and \( \gamma_e \), and for any combination of \( \gamma_b \) and \( \gamma_e \) which falls inside this boundary the UBF constraint is satisfied. Indeed if the cosine terms in (18) are approximated by the first two terms of a Taylor series expansion, then this quadratic relationship becomes clearer and it can be shown that in general (18) can be written in the form:

\[
a \gamma_b^2 + b \gamma_b \gamma_e + c \gamma_e^2 + d \quad (19)
\]

Where:

\[
a = \frac{(p_b + \rho_b \rho_c)}{2(1 + \rho_b + \rho_c)}, \quad b = \frac{\rho_c + \rho_b \rho_c}{2(1 + \rho_b + \rho_c)}, \quad c = \frac{\rho_b \rho_c}{(1 + \rho_b + \rho_c)}, \quad d = \frac{1 + \rho_b^2 + \rho_c^2 - \rho_b \rho_c - \rho_b \rho_c}{(1 + \rho_b + \rho_c)^2}
\]

For the constraint to be satisfied, \( \gamma_b \) and \( \gamma_e \) must satisfy the following relationship:

\[
a \gamma_b^2 + b \gamma_b \gamma_e + c \gamma_e^2 + d \leq (0.02)^2 \quad (20)
\]

If the relationship between \( \gamma_b \) and \( \gamma_e \) and the magnitude of neutral current were now known then (20) could be used to find the optimum combination of \( \gamma_b \) and \( \gamma_e \) which minimizes the neutral current and satisfies the UBF constraint. However, the relationship between \( \gamma_b \) and \( \gamma_e \) and the magnitude of neutral current is not easy to obtain in real time, therefore a simpler approach to setting \( \gamma_b \) and \( \gamma_e \) is required. Instead, here we will consider \( \gamma_b \) and \( \gamma_e \) to be constrained to have a linear relationship given by:

\[
|\gamma_b| + |\gamma_e| \leq \gamma_{\text{max}} \quad (21)
\]

Where \( \gamma_{\text{max}} \) is the smaller of the intercepts of (20) on the \( \gamma_b, \gamma_e \) axes which is easily obtained as:

\[
\gamma_{\text{max}} = \min \left( \frac{0.02^2 - d}{a}, \frac{0.02^2 - d}{b} \right) \quad (22)
\]

For \( \rho_b, \rho_c \) with the limits of [1, 0.98], \( \gamma_{\text{max}} \) has a small variation within the limits of [3.334°, 3.438°].

In summary, as shown in Fig. 4, the determination of the phase shift required to minimize neutral current can follow two steps. In the first step the phase shift angle required to give zero neutral current are calculated from (4). If these phase shifting angles calculated from (4) are within the phase shift constraint area, then they can be used directly as the reference phase shifting angle.
However if the phase shifting angle calculated from (4) violates the constraints, in order to find out the reference phase shift for the minimum neutral current, look for which derivative $\frac{\partial n}{\partial \gamma_b}$ or $\frac{\partial n}{\partial \gamma_c}$ is the larger and then set this phase shift at its maximum value, and the other equal to 0 as is indicated in Fig. 4. The maximum value for the phase shift angle is 3.24° as shown in (22).

Fig. 5 summarizes the detail of the method used for the neutral current minimization control. The voltage amplitude control section provides the amplitude of the reference voltage and also maintains the amplitude unbalance constraints. The amplitudes of the voltages are regulated to make the amplitudes of the resulting currents meet the constraints as obtained in phase shift control: $I_a \leq I_b + I_c$, $I_b \leq I_a + I_c$, $I_c \leq I_a + I_b$. After the phase currents meet the phase current amplitude constraints, the phase shifting control section provides the phase angle reference, where the phase shifts are chosen as described above to either eliminate or minimize neutral current while also satisfying the phase unbalance degree constraints.

![Fig. 5. The neutral current minimization control with references voltage.](image)

V. HARDWARE VALIDATION

In order to verify the control approaches described above, a 220 V, 2 kVA downscaled prototype of a three-phase four-leg inverter has been implemented with the hardware in the loop real time simulation platform from Opal-RT as shown in Fig.6. In this case the control algorithms and PWM are implemented in the OP5600 Series OPAL-RT simulators which are generated from the Matlab/Simulink models. The OP5600 Series OPAL-RT simulator also generates the firing pulses for the 4-leg inverter bridge which is based on the 8857-1 IGBT Chopper/Inverter from Lab-volt. As the key stage for dealing with the unbalanced load for SST is the output stage, for the sake of simplicity, only the inverter stage of the SST is implemented in the experiment. The tests compare results from two control methods, neutral current elimination control, and neutral current minimization control with limitations according to the EN 50160 unbalance standard. The parameters in Table. I are fixed for all of these tests. The recorded waveforms are inverter neutral current $i_n$, and output phase voltage $V_a$, $V_b$, $V_c$.

![Fig. 6. Downscaled prototype of a three-phase four-leg inverter.](image)

### TABLE I

<table>
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<th>Simulation Parameters</th>
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<td><strong>Rated Power</strong></td>
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<td><strong>DC Bus Voltage</strong></td>
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<td><strong>Load Resistors</strong></td>
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<tr>
<td><strong>Output Voltage</strong></td>
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<tr>
<td><strong>Switching Frequency</strong></td>
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<td><strong>LC Filter</strong></td>
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A. Neutral Current Elimination Control

For neutral current elimination control, the output voltage has a larger variation on its amplitude than its phase angle as shown in the Fig. 7 (a). Because there was no limitation set for the voltage amplitude adjustment, the voltage amplitude regulation part in the control will adjust the amplitude of output voltage limited only by the phase current constraint. However, the phase shifting part regulates the phase angle to achieve the elimination of the neutral current. The result of elimination control is the almost total elimination of the neutral current after several cycles, although of course at the price of a very significant degree of unbalance in SST output voltage amplitude. Note the neutral current is not zero, because the experiment omits the correction for load angle.
B. Neutral Current Minimization Control with Constraints

Fig. 8 shows the test results for the application of the neutral current minimization where the output voltage unbalance in amplitude and phase is limited to be within the constraints set by the standard. Clearly the unbalance in the three phase output voltages is much less noticeable and the neutral current is successfully reduced, but not eliminated. Fig. 8 (a) shows a close up of the voltage at the instant of minimization control activation. Unlike neutral current elimination control, which requires time to coordinate the amplitude and phase, the neutral current minimization control can stabilize within one cycle. Fig. 8 (b) shows the longer time scale of the experiment with four sections. The load is balanced at the start with balanced voltage control, after which the load becomes unbalanced initially with balanced voltage control and then with the neutral current minimization control active, and finally returning back to balanced load at the end. This illustrates that the control not only succeeds in reducing the neutral current but also works on dynamic load variations. Table II compares the results of SST with voltage balance, neutral current elimination and neutral current minimization control under one set of unbalanced loads.

Table II: Experiment Results Summary For Different Controls

<table>
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<tr>
<th>Control Strategy</th>
<th>Neutral Current Amplitude (A)</th>
<th>Neutral Current Reduction (%)</th>
<th>Voltage Unbalance (%)</th>
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<tr>
<td>Balanced Voltage Control</td>
<td>2.1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>Neutral Current Minimization</td>
<td>1.3</td>
<td>38%</td>
<td>1.5</td>
</tr>
<tr>
<td>Neutral Current Elimination</td>
<td>0.3</td>
<td>86%</td>
<td>62.3</td>
</tr>
</tbody>
</table>

Fig. 9 shows the neutral current minimization control under an asymmetric open fault test, where the load starts from a balanced load and subsequently has a single phase open fault as indicated by the sudden increase in neutral current. However in the face of the open fault, the control maintains the voltage balance thus validating that the proposed neutral current minimization can run stably in both normal operation and open faults.

VI. PERFORMANCE IN DISTRIBUTION NETWORK

Although the basic operation of the control has been validated in the previous section, it is now important to investigate the potential performance improvements obtained when the control is applied with degrees of load unbalance closer to those which occur in the distribution system. In order to validate and quantitatively compare the performance of these control strategies for more realistic degrees of load unbalance, simulation results are obtained for the unbalanced loading profile for a 400 kVA, 10 kV/400 V ENWL distribution network in the UK, consisting of 90 residential customers evenly distributed across three phases, as shown in Fig. 10 [33]. A winter day unbalanced three phase loading profile in each area with 1 minute time resolution based on the average yearly energy consumption is shown in Fig. 11 (left). The load is modelled as an exponential load with its voltage sensitivity set as indicated in Fig. 11 (right) [20][32].

A. Network Dynamic Simulation

The network model is implemented in Matlab/Simulink, with a dynamic and continuously changing load. Fig. 11 shows the data for the total power consumption in each area, while the data for each customer is obtained by averaging the power in the corresponding phase and then randomizing between 90% to 110%. To speed up the simulation, the one
minute resolution in data is downscaled to 1 second in the simulation, i.e. 24 hour is represented by 1440 seconds. The SST is represented by a controllable voltage source with the balanced voltage control, neutral current elimination control and proposed neutral current minimization control.

Fig. 12 (a) shows the neutral current result at the SST terminal under the various controls. Clearly, compared with the balanced voltage control, the proposed neutral current minimization control reduces the neutral current, while the neutral current elimination control can eliminate the neutral current as expected. However, the latter one has a significant voltage unbalance degree in terms of both PVUR and UBF as shown in Fig. 12 (b) and (c) respectively. In particular it violates the PVUR standard all the time and peaks to approximately 115%. On the other hand the neutral current minimization control can maintain both PVUR and UBF below the 2% limit for the entire time.

Fig. 11. Winter daily three phase loading profile in area 1, 2 and 3.

B. Network Static Simulation

The SST control algorithm regulates its output voltage in order to minimize the neutral current at the SST terminals although clearly this is not the same neutral current which will exist in all the other lines. Therefore, it is important to look at the effect of the neutral current minimization on the neutral current and losses in the other lines. The losses in the various sections of lines are calculated by determining the current and voltage for the various lines from the SST output voltage and the various loads in the different areas. The full network is too complex to present the neutral current in each line section, and therefore a simplified model with lines representing three areas as shown in Fig. 13 is used. The corresponding network data is given in the figure and the load data is the same as Fig. 11. Power flow analysis is performed using the Matlab fsolve function assuming static loads for each one minute of time resolution.

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large loading area. Of course since the total neutral current is reduced, then if an increase occurs on some lines, this must be counteracted by a greater decrease on other lines.

The test results of total power energy consumption for balanced voltage and neutral current minimization control is summarized and compared in Table. III

In this situation, the neutral current minimization control not only reduces the loss from the cables but also the output load power. Compared with the savings from loss in the lines, considerably more energy is saved from the loading. This saving is attributed to the voltage amplitude adjustment aspect of the neutral current minimization control, which can decrease the voltage amplitude and hence reduce the power in the residential load.

### TABLE III

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<thead>
<tr>
<th>Daily Energy</th>
<th>Balance Voltage Control</th>
<th>Neutral Current Minimization</th>
<th>Energy Saving</th>
</tr>
</thead>
<tbody>
<tr>
<td>Output (kWh)</td>
<td>2027.8</td>
<td>1988</td>
<td>39.7 (1.96%)</td>
</tr>
<tr>
<td>Loading (kWh)</td>
<td>1978</td>
<td>1941</td>
<td>37.2 (1.88%)</td>
</tr>
<tr>
<td>Loss (kWh)</td>
<td>49.4</td>
<td>46.9</td>
<td>2.5 (5.06%)</td>
</tr>
</tbody>
</table>

Three control strategies have been evaluated for the SST output voltage which can eliminate the neutral current. The most effective of these is the combination control which works by making adjustment to both voltage amplitude and phase in order to eliminate neutral current. However, these techniques are not practical in reality because the level of load unbalance present in the distribution network is such that very large unbalance in the SST output voltage and phase would be required in order to totally eliminate the neutral current.

Therefore, a neutral current minimization control technique
had been developed which minimizes neutral current but still ensures that SST output voltage magnitude and phase unbalance are within the constraints imposed by the EN 50160 standard. The operation of the control has been validated by hardware tests. It has been shown that under the typical range of load unbalance seen in the distribution network this control approach can both reduce neutral current and ensure adherence to the standards.

The neutral current minimization control has then been applied to a model of a distribution network with distributed loads and a time varying daily loading profile. The results of this analysis have shown that although neutral current will not be minimized in all parts of the network, overall the neutral current minimization results in an energy saving from both reduced losses in the distribution cables and reduced power consumption in the load. The reduction in cable loss was shown to be of the order of 5.1%. The reduction of load power consumption was shown to be of the order of 1.9%. Therefore, the SST with the neutral current minimization control can potentially give significant energy savings in the distribution network.

VIII. REFERENCES

Junru Chen (S’17) received his ME Electrical Energy Engineering from University College Dublin in 2016. Since September 2016, he is a PhD student candidate with University College Dublin. His scholarship is funded through the SFI Investigator Award with title “Energy Systems Integration Partnership Programme”. His current research interests in power electronics control, modelling, stability and application.

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Cathal O’Loughlin was born in Dublin in 1968. He received a B.E. Degree in electrical engineering from University College, Dublin in 1990, and the MEngSc Degree in 1993. He joined Merrimack Transformers Ireland in 1993 as a design engineer where he remained until 2003. He was awarded Techstart Employee of the year 1994. After a period in non-engineering work he returned in 2010 as a researcher in a project to design, build and test a linear switched reluctance generator at the Institute of Technology, Blanchardstown, Dublin for 2 years. He then worked in Wavebob, a company involved with wave energy devices as a design engineer for a short period and then lectured Mathematics for 1 year in the Institute of Technology, Carlow (2012-2013). He has since been as a research Engineer in the Energy Institute, UCD. His research interests are electrical machines, power electronics and real time implementation.

Terence O’Donnell (M’1995) is an associate professor in the School of Electrical and Electronic Engineering in University College Dublin. He is a principle investigator within the UCD Energy Institute where his research interests are focused on the use of power electronics converters in power systems and in particular on the interfacing of power electronics to the grid. Specific interests include the grid applications of solid state transformers, the control of converters for distributed energy resources and the use of power hardware in the loop testing methods.

Terence has previously worked in the Tyndall National Institute in Cork where he has worked on numerous research projects, both at national and international level, relating to design, modelling and fabrication of magnetics for power conversion, magnetic field sensors, inductive powering and energy harvesting. Terence is an author on over 50 publications in peer reviewed journals.